

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki, et al. Art Unit : 2871
Serial No. : 09/126,826 Examiner : Dung T. Nguyen
Filed : July 31, 1998
Title : METHOD FOR PRODUCING DISPLAY DEVICE

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

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Date: May 9, 2005

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-019002	Application No. 09/126,826
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Shunpei Yamazaki, et al.	
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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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	AB	4,065,781	12/27/1977	Gutknecht			06/21/1974
	AC	4,068,020	01/10/1978	Reuschel			12/29/1975
	AD	4,103,297	07/25/1978	McGreivy et al.			12/20/1976
	AE	4,239,346	12/16/1980	Lloyd			05/23/1979
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

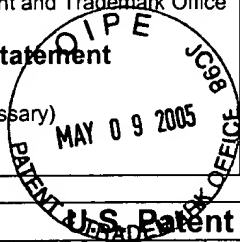
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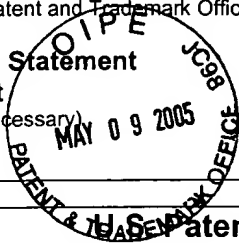
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	DP	2005/0045884	03/03/2005	Yamazaki et al.			05/25/2004

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	DQ	EP 231953	08/12/1987	JAPAN			In English	
	DR	JP 53-144297	12/15/1978	JAPAN			Abstract	
	DS	JP 55-032026	03/06/1980	JAPAN			Full	
	DT	JP 60-033863	02/21/1985	JAPAN			Abstract	
	DU	JP 60-035574	02/23/1985	JAPAN			Abstract	
	DV	JP 60-066863	04/17/1985	JAPAN			Abstract	
	DW	JP 60-066865	04/17/1985	JAPAN			Abstract	
	DX	JP 61-141174	06/28/1986	JAPAN			Full	
	DY	JP 63-100777	05/02/1988	JAPAN			Abstract	
	DZ	JP 63-279228	11/16/1988	JAPAN			Full	
	EA	JP 64-030272	02/01/1989	JAPAN			Abstract	

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							Yes	No
	EB	JP 64-068724	03/14/1989	JAPAN			Abstract	
	EC	JP 01-130131	05/23/1989	JAPAN			Abstract	
	ED	JP 01-156725	06/20/1989	JAPAN			Full	
	EE	JP 02-051129	02/21/1990	JAPAN			Abstract	
	EF	JP 02-103925	04/17/1990	JAPAN			Abstract	
	EG	JP 02-188723	07/24/1990	JAPAN			Abstract	
	EH	JP 02-234134	09/17/1990	JAPAN			Full	
	EI	JP 04-184424	07/01/1992	JAPAN			Abstract	
	EJ	JP 06-163958	06/10/1994	JAPAN			Abstract	

Other Documents (include Author, Title, Date, and Place of Publication)

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	EK	Licari, "PLASTIC COATINGS FOR ELECTRONICS", McGraw-Hill Book Company, Chapter II, 1970, pp. 57-64.
	EL	Yeh et al., "Novel Technique For SiO ₂ Formed By Liquid-Phase Deposition For Low-Temperature Processed Polysilicon TFT", IEEE Electron Device Letters, Vol. 14, No. 8, 1993, PP. 403-405.
	EM	
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